

Abstracts

Analysis of Multilayer Interconnection Lines for a High-Speed Digital Integrated Circuit

Y. Fukuoka, Q. Zhang, D.P. Neikirk and T. Itoh. "Analysis of Multilayer Interconnection Lines for a High-Speed Digital Integrated Circuit." 1985 Transactions on Microwave Theory and Techniques 33.6 (Jun. 1985 [T-MTT]): 527-532.

A general method for the analysis of multilayer interconnection lines is presented. This method is capable of predicting frequency dispersion of the transmission-line parameters and is useful for accurately investigating the coupling phenomena among adjacent lines and the input and output impedance relations.

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